

PATENT
03DV-7118

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Alan Joseph Janicek et al.

Serial No.: 10/065,669

Filed: November 7, 2002

For: METHOD AND APPARATUS FOR COMBINING
PTCR/OL AND RUN CAPACITOR

:
:
:
:
:
:
:
:
:
:
:
:

Art Unit: 2831
Examiner: Ha, Nyugen T.

COMMENTS ON STATEMENTS OF REASONS FOR ALLOWANCE

Mail Stop: Issue Fee
Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir/Madam:

In response to the Notice of Allowability dated May 22, 2006, Applicants submit the following remarks.

Remarks

Claims 18-33 are allowed.

Applicants respectfully disagree with a statement of reasons for allowance on page 2 of the Notice of Allowability. The statement states, "With respect to Claims 18-33, the prior art alone or in combination does not teach the limitation of... plurality of sidewalls that extend from a first surface... receive a run capacitor." Applicants respectfully submit that Claim 18 does not include, "plurality of sidewalls that extend from a first surface", as stated in the statement. Rather, Claim 18 includes, "a plurality of sidewalls that extend from said first surface and are integrally formed with said first surface, said sidewalls extend from said first surface and form a compartment that is sized to at least partially receive a run capacitor therein".

Favorable action is respectfully solicited.

Respectfully Submitted,



Patrick W. Rasche
Registration No. 37,916
ARMSTRONG TEASDALE LLP
One Metropolitan Square, Suite 2600
St. Louis, Missouri 63102-2740
(314) 621-5070